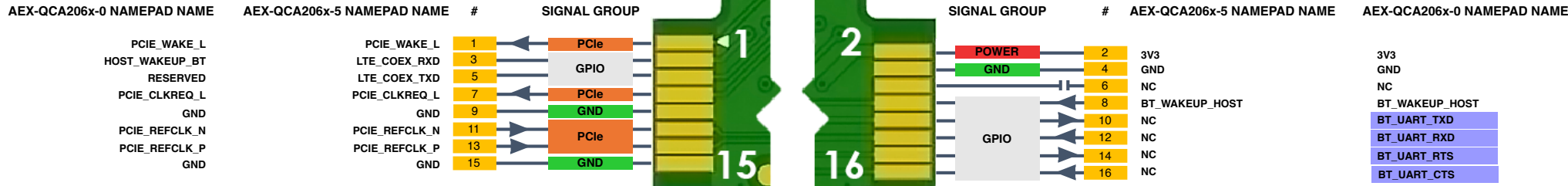
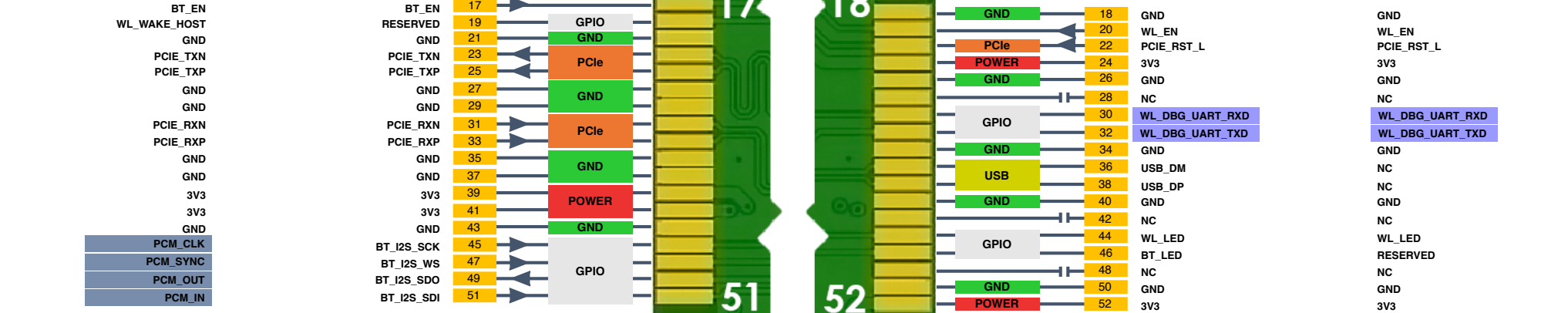


A



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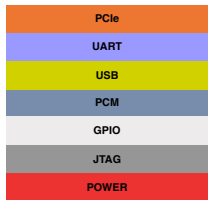


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SIGNAL GROUPS



\* Keep floating No Connection [NC] signals and those unused.  
\*\* Internal pull-up is recommended for all 3V3 I/Os. All others at VDDIO (1V8).



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PART NUMBER		DESCRIPTION															
AXX-QCA206X-XXXX		AXX-QCA206X-XXXX E20 Series X, Mini PCIe Pin map															
AEBPO-190004-03		Andy															
DRAWING NUMBER		FINISH by		APVD by		MATERIAL by		UNITS in		DIMENSION		SIZE		SCALE		SHEET	
AEBPO-190004-03		Andy		LT				mm		1PCL ±0.5		A4		1:1		1 OF 3	
RELEASE	Zone	NOTES				DRAWN	CHECKED	DATE									
1	1	Initial Release				NM	LT	2023-02-08									
2	1	Separation of AEX-QCA206x-0 and AEX-QC206x-5				SC	LT	2023-05-22									
3	1	Described reserved pins				SC	LT	2024-01-30									



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AEX-QCA206x-0Wxx Modules				AEX-QCA206x-5Wxx Modules			
FUNCTION	PAD NAME	I/O	PAD #	I/O	PAD NAME	FUNCTION	
Wi-Fi PCIe Wake up signal. Internal pull up 3.3V power domain.	PCIE_WAKE_L	O	1	O	PCIE_WAKE_L	Wi-Fi PCIe Wake up signal. Internal pull up 3.3V power domain.	
3.3V main power supply input	3V3	I	2	I	3V3	3.3V main power supply input	
Host Bluetooth Wake	HOST_WAKEUP_BT	GPIO_10	3	GPIO_10	LTE_COEX_RXD	LTE Coex Receive	
Ground	GND		4	GND	GND	Ground	
Reserved for LAA COEX TXD	Reserved	GPIO_11	5	GPIO_11	LTE_COEX_TXD	LTE Coex Transmit	
No Connection	NC		6	NC	NC	No Connection	
Wi-Fi PCIe Clock Request Low Active. Pull up 3.3V power domain.	PCIE_CLKREQ_L	O	7	O	PCIE_CLKREQ_L	Wi-Fi PCIe Clock Request Low Active. Pull up 3.3V power domain.	
Host Bluetooth UART Wake	BT_WAKEUP_HOST	GPIO_9	8	GPIO_9	BT_WAKEUP_HOST	Host Bluetooth UART Wake	
Ground	GND		9	GND	GND	Ground	
Bluetooth UART Transmit Data	BT_UART_TXD	O	10	GPIO_1	NC	No Connection	
PCIe Reference Clock Negative	PCIE_REFCLK_N	I	11	I	PCIE_REFCLK_N	PCIe Reference Clock Negative	
Bluetooth UART Receive Data	BT_UART_RXD	I	12	GPIO_0	NC	No Connection	
PCIe Reference Clock Positive	PCIE_REFCLK_P	I	13	I	PCIE_REFCLK_P	PCIe Reference Clock Positive	
Bluetooth UART Request To Send	BT_UART_RTS	O	14	GPIO_3	NC	No Connection	
Ground	GND		15	GND	GND	Ground	
Bluetooth UART Clear To Send	BT_UART_CTS	I	16	GPIO_2	NC	No Connection	
Active high to enable Bluetooth. Pull up 3.3V power domain.	BT_EN	I	17	I	BT_EN	Active high to enable Bluetooth. Pull up 3.3V power domain.	
Ground	GND		18	GND	GND	Ground	
WiFi Wake Host	WL_WAKE_HOST	GPIO_49	19	GPIO_49	Reserved	Reserved for WL WAKE HOST or WL SEN UART RXD	
Active high to enable Wi-Fi. Pull up 3.3 V power domain.	WL_EN	I	20	I	WL_EN	Active high to enable Wi-Fi. Pull up 3.3 V power domain.	
Ground	GND		21	GND	GND	Ground	
Wi-Fi PCIe reset signal. Internal pull up 3.3V power domain.	PCIE_RST_L	I	22	I	PCIE_RST_L	Wi-Fi PCIe reset signal. Internal pull up 3.3V power domain.	
PCIe Transmitting Negative	PCIE_TXN	O	23	O	PCIE_TXN	PCIe Transmitting Negative	
3.3V main power supply input	3V3	I	24	I	3V3	3.3V main power supply input	
PCIe Transmitting Positive	PCIE_TXP	O	25	O	PCIE_TXP	PCIe Transmitting Positive	
Ground	GND		26	GND	GND	Ground	

<b>PART NUMBER</b>	<b>DESCRIPTION</b>							
AXX-QCA206X-XXXX	AXX-QCA206X-XXXX E20 Series X, Mini PCIe Pin map							
								
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<b>DRAWING NUMBER</b>	<b>FINISH by</b>	<b>APVD by</b>	<b>MATERIAL by</b>	<b>UNITS in</b>	<b>DIMENSION</b>	<b>SIZE</b>	<b>SCALE</b>	<b>SHEET</b>
AEBPO-190004- 03	Andy	LT		mm	1PCL ±0.5	A4	1:1	2 OF 3
<b>RELEASE</b>	<b>Zone</b>	<b>NOTES</b>	<b>DRAWN</b>	<b>CHECKED</b>	<b>DATE</b>			
1	1	Initial Release	NM	LT	2023-02-08			
2	1	Separation of AEX-QCA206x-0 and AEX-QC206x-5	SC	LT	2023-05-22			
3	1	Described reserved pins	SC	LT	2024-01-30			

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E

## AEX-QCA206x-0Wxx Modules

## AEX-QCA206x-5Wxx Modules

FUNCTION	PAD NAME	I/O	PAD #	I/O	PAD NAME	FUNCTION
Ground	GND		27		GND	Ground
No Connection	NC		28		NC	No Connection
Ground	GND		29		GND	Ground
Debug Wi-Fi UART Receive	WL_DBG_UART_RXD	GPIO_45	30	GPIO_45	WL_DBG_UART_RXD	Debug Wi-Fi UART Receive
PCIE Receiving Negative	PCIE_RXN	I	31	I	PCIE_RXN	PCIE Receiving Negative
Debug Wi-Fi UART Transmit	WL_DBG_UART_TXD	GPIO_44	32	GPIO_44	WL_DBG_UART_TXD	Debug Wi-Fi UART Transmit
PCIE Receiving Positive	PCIE_RXP	I	33	I	PCIE_RXP	PCIE Receiving Positive
Ground	GND		34		GND	Ground
Ground	GND		35		GND	Ground
No Connection	NC		36		USB_DM	USB Data Negative differential pair
Ground	GND		37		GND	Ground
No Connection	NC		38		USB_DP	USB Data Positive differential pair
3.3V main power supply input	3V3	I	39	I	3V3	3.3V main power supply input
Ground	GND		40		GND	Ground
3.3V main power supply input	3V3	I	41	I	3V3	3.3V main power supply input
No Connection	NC		42		NC	No Connection
Ground	GND		43		GND	Ground
Operates the Wifi LED	WL_LED	GPIO_54	44	GPIO_54	WL_LED	Operates the Wifi LED
PCM Clock	PCM_CLK	GPIO_7	45	GPIO_7	BT_I2S_SCK	
Reserved for BT LED or I2C CLK	Reserved	GPIO_13	46	GPIO_13	BT_LED	Operates the Bluetooth LED
PCM SYNC	PCM_SYNC	GPIO_6	47	GPIO_6	BT_I2S_WS	
No Connection	NC		48		NC	No Connection
PCM OUT	PCM_OUT	GPIO_5	49	GPIO_5	BT_I2S_SDO	
Ground	GND		50		GND	Ground
PCM IN	PCM_IN	GPIO_4	51	GPIO_4	BT_I2S_SDI	
3.3V main power supply input	3V3	I	52	I	3V3	3.3V main power supply input

## PART NUMBER

## DESCRIPTION

AXX-QCA206X-XXXX

AXX-QCA206X-XXXX E20 Series X, Mini PCIe Pin map



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## DRAWING NUMBER

## FINISH by

## APVD by

## MATERIAL by

## UNITS in

## DIMENSION

## SIZE

## SCALE

## SHEET

AEBPO-190004-03

Andy

LT

mm

1PCL ±0.5

A4

1:1

3 OF 3

## RELEASE

## Zone

## NOTES

## DRAWN

## CHECKED

## DATE

1

1

Initial Release

NM

LT

2023-02-08

2

1

Separation of AEX-QCA206x-0 and AEX-QC206x-5

SC

LT

2023-05-22

3

1

Described reserved pins

SC

LT

2024-01-30

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