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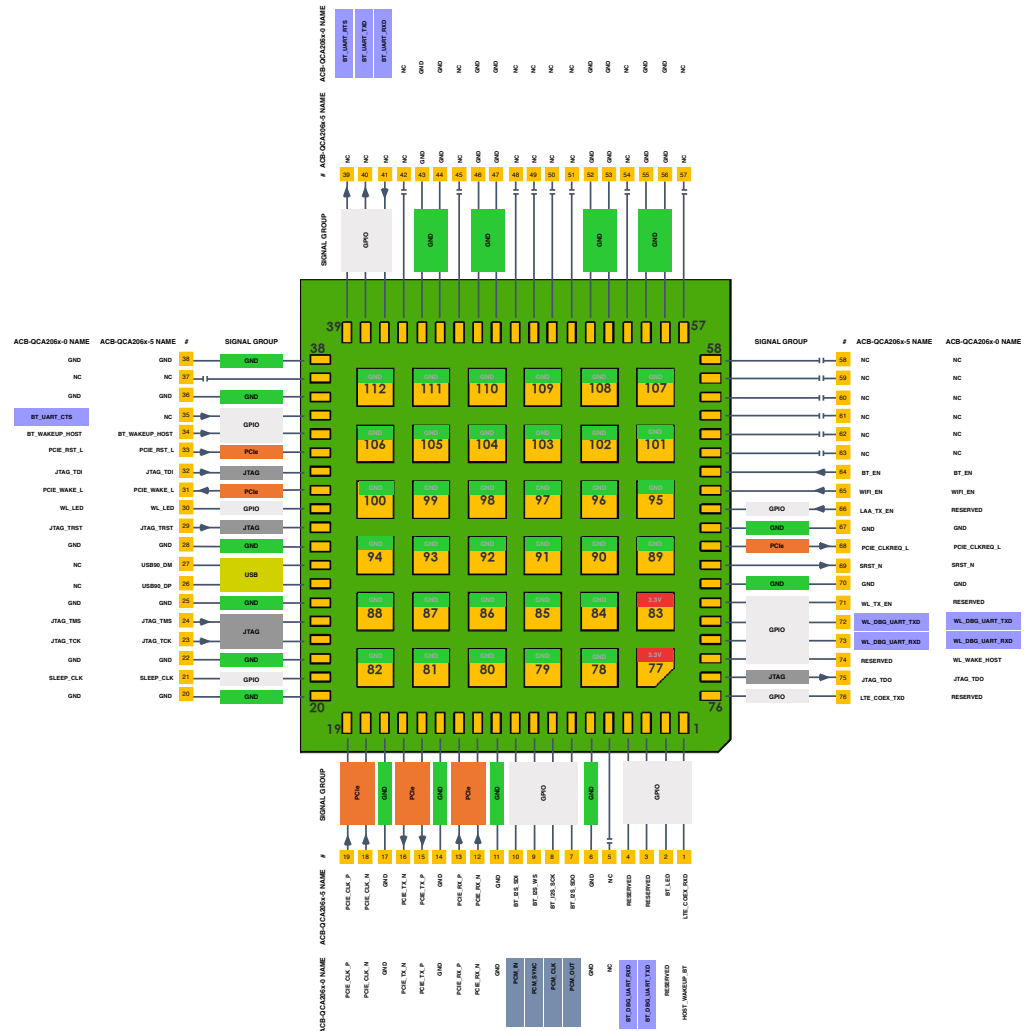
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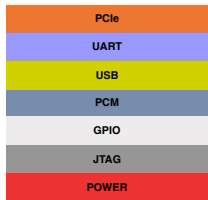
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SIGNAL GROUPS



* Keep floating No Connection [NC] signals and those unused.
 ** Internal pull-up is recommended for all 3V3 I/Os. All others at VDDIO (1V8).

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PART NUMBER	DESCRIPTION
ACB-QCA206X-XXXX	ACB-QCA206X-XXXX E20B CoB Pinout map



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DRAWING NUMBER	FINISH by	APVD by	MATERIAL by	UNITS in	DIMENSION	SIZE	SCALE	SHEET
AEBPO-190001-03	Andy	LT		mm	1PCL ±0.5	A4	1:1	1 OF 4

RELEASE	Zone	NOTES	DRAWN	CHECKED	DATE
1	1	Initial Release	NM	LT	2023-02-08
2	1	Separation of ACB-QCA206x-0 and ACB-QC206x-5	SC	LT	2023-05-22
3	1	Described reserved pins	SC	LT	2024-01-30

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ACB-QCA206x-0Wxx Modules				ACB-QCA206x-5Wxx Modules			
FUNCTION	PAD NAME	I/O	PAD #	I/O	PAD NAME	FUNCTION	
Host Bluetooth Wake	HOST_WAKEUP_BT	GPIO_10	1	GPIO_10	LTE_COEX_RXD	LTE Coex Receive	
Reserved for BT LED or I2C CLK	Reserved	GPIO_13	2	GPIO_13	BT_LED	Operates the Bluetooth LED	
Debug Bluetooth UART Transmit	BT_DB_UART_TXD	GPIO_13	3	GPIO_13	Reserved	Reserved for I2C CLK	
Debug Bluetooth UART Receive	BT_DB_UART_RXD	GPIO_12	4	GPIO_12	Reserved	Reserved for I2C DATA	
No Connection	NC		5		NC	No Connection	
Ground	GND		6		GND	Ground	
PCM OUT	PCM_OUT	GPIO_5	7	GPIO_5	BT_I2S_SDO		
PCM Clock	PCM_CLK	GPIO_7	8	GPIO_7	BT_I2S_SCK		
PCM SYNC	PCM_SYNC	GPIO_6	9	GPIO_6	BT_I2S_WS		
PCM IN	PCM_IN	GPIO_4	10	GPIO_4	BT_I2S_SDI		
Ground	GND		11		GND	Ground	
PCIE Receiving Negative	PCIE_RX_N	I	12	I	PCIE_RX_N	PCIE Receiving Negative	
PCIE Receiving Positive	PCIE_RX_P	I	13	I	PCIE_RX_P	PCIE Receiving Positive	
Ground	GND		14		GND	Ground	
PCIE Transmitting Positive	PCIE_TX_P	O	15	O	PCIE_TX_P	PCIE Transmitting Positive	
PCIE Transmitting Negative	PCIE_TX_N	O	16	O	PCIE_TX_N	PCIE Transmitting Negative	
Ground	GND		17		GND	Ground	
PCIE Reference Clock Negative	PCIE_CLK_N	I	18	I	PCIE_CLK_N	PCIE Reference Clock Negative	
PCIE Reference Clock Positive	PCIE_CLK_P	I	19	I	PCIE_CLK_P	PCIE Reference Clock Positive	
Ground	GND		20		GND	Ground	
External 32.768 KHz sleep clock input	SLEEP_CLK	GPIO_14	21	GPIO_14	SLEEP_CLK	External 32.768 KHz sleep clock input	
Ground	GND		22		GND	Ground	
JTAG TCK Signal	JTAG_TCK	I	23	I	JTAG_TCK	JTAG TCK Signal	
JTAG TMS Signal	JTAG_TMS	I	24	I	JTAG_TMS	JTAG TMS Signal	
Ground	GND		25		GND	Ground	
No Connection	NC		26		USB90_DP	USB Data Positive differential pair	
No Connection	NC		27		USB90_DM	USB Data Negative differential pair	
Ground	GND		28		GND	Ground	
JTAG TRST Signal	JTAG_TRST	I	29	I	JTAG_TRST	JTAG TRST Signal	
Operates the Wi-Fi LED	WL_LED	GPIO_54	30	GPIO_54	WL_LED	Operates the Wi-Fi LED	
Wi-Fi PCIE Wake up signal. Internal pull up 3.3V power domain.	PCIE_WAKE_L	O	31	O	PCIE_WAKE_L	Wi-Fi PCIE Wake up signal. Internal pull up 3.3V power domain.	
JTAG TDI Signal	JTAG_TDI	I	32	I	JTAG_TDI	JTAG TDI Signal	
Wi-Fi PCIE reset signal. Internal pull up 3.3V power domain.	PCIE_RST_L	I	33	I	PCIE_RST_L	Wi-Fi PCIE reset signal. Internal pull up 3.3V power domain.	
Host Bluetooth Wake	BT_WAKEUP_HOST	GPIO_9	34	GPIO_9	BT_WAKEUP_HOST	Host Bluetooth Wake	
Bluetooth UART Clear To Send	BT_UART_CTS	I	35	GPIO_2	NC	No Connection	
Ground	GND		36		GND	Ground	
No Connection	NC		37		NC	No Connection	
Ground	GND		38		GND	Ground	

PART NUMBER	DESCRIPTION								
ACB-QCA206X-XXXX	ACB-QCA206X-XXXX E20B CoB Pinout map								
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DRAWING NUMBER	FINISH by	APVD by	MATERIAL by	UNITS in	DIMENSION	SIZE	SCALE	SHEET	
AEBPO-190001- 03	Andy	LT		mm	1PCL ±0.5	A4	1:1	2 OF 4	
RELEASE	Zone	NOTES			DRAWN	CHECKED	DATE		
1	1	Initial Release			NM	LT	2023-02-08		
2	1	Separation of ACB-QCA206x-0 and ACB-QC206x-5			SC	LT	2023-05-22		
3	1	Described reserved pins			SC	LT	2024-01-30		

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ACB-QCA206x-0Wxx Modules				ACB-QCA206x-5Wxx Modules			
FUNCTION	PAD NAME	I/O	PAD #	I/O	PAD NAME	FUNCTION	
Bluetooth UART Request To Send	BT_UART_RTS	O	39	GPIO_3	NC	No Connection	
Bluetooth UART Transmit Data	BT_UART_TXD	O	40	GPIO_1	NC	No Connection	
Bluetooth UART Receive Data	BT_UART_RXD	I	41	GPIO_0	NC	No Connection	
No Connection	NC		42	NC	NC	No Connection	
Ground	GND		43	GND	GND	Ground	
Ground	GND		44	GND	GND	Ground	
No Connection	NC		45	NC	NC	No Connection	
Ground	GND		46	GND	GND	Ground	
Ground	GND		47	GND	GND	Ground	
No Connection	NC		48	NC	NC	No Connection	
No Connection	NC		49	NC	NC	No Connection	
No Connection	NC		50	NC	NC	No Connection	
No Connection	NC		51	NC	NC	No Connection	
Ground	GND		52	GND	GND	Ground	
Ground	GND		53	GND	GND	Ground	
No Connection	NC		54	NC	NC	No Connection	
Ground	GND		55	GND	GND	Ground	
Ground	GND		56	GND	GND	Ground	
No Connection	NC		57	NC	NC	No Connection	
No Connection	NC		58	NC	NC	No Connection	
No Connection	NC		59	NC	NC	No Connection	
No Connection	NC		60	NC	NC	No Connection	
No Connection	NC		61	NC	NC	No Connection	
No Connection	NC		62	NC	NC	No Connection	
No Connection	NC		63	NC	NC	No Connection	
Active high to enable Bluetooth. Pull up 3.3V power domain.	BT_EN	I	64	I	BT_EN	Active high to enable Bluetooth. Pull up 3.3V power domain.	
Active high to enable Wi-Fi. Pull up 3.3 V power domain.	WIFI_EN	I	65	I	WIFI_EN	Active high to enable Wi-Fi. Pull up 3.3V power domain.	
Reserved for LAA TX EN	Reserved	GPIO_40	66	GPIO_40	LAA_TX_EN	LTE license assisted access transmitting enable. When high the 5GHz receiver is in a protected state.	
Ground	GND		67	GND	GND	Ground	
Wi-Fi PCIe Clock Request Low Active. Pull up 3.3V power domain.	PCIE_CLKREQ_L	O	68	O	PCIE_CLKREQ_L	Wi-Fi PCIe Clock Request Low Active. Pull up 3.3V power domain.	
JTAG System Reset signal. Active Low	SRST_N	O	69	O	SRST_N	JTAG System Reset signal. Active Low	
Ground	GND		70	GND	GND	Ground	
Reserved for WL TXEN LAA	Reserved	GPIO_41	71	GPIO_41	WL_TX_EN	Wireless Lan transmitting enable. When high, the LAA receiver is placed in a protected state.	
Debug Wi-Fi UART Transmit	WL_DBG_UART_TXD	GPIO_44	72	GPIO_44	WL_DB_UART_TXD	Debug Wi-Fi UART Transmit	
Debug Wi-Fi UART Receive	WL_DBG_UART_RXD	GPIO_45	73	GPIO_45	WL_DB_UART_RXD	Debug Wi-Fi UART Receive	
Wi-Fi Wake Host	WL_WAKE_HOST	GPIO_49	74	GPIO_49	Reserved	Reserved for WL WAKE HOST or WL SEN UART RXD	
JTAG TDO Signal	JTAG_TDO	O	75	O	JTAG_TDO	JTAG TDO Signal	

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RELEASE	Zone	NOTES			DRAWN	CHECKED	DATE	
1	1	Initial Release			NM	LT	2023-02-08	
2	1	Separation of ACB-QCA206x-0 and ACB-QC206x-5			SC	LT	2023-05-22	
3	1	Described reserved pins			SC	LT	2024-01-30	

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ACB-QCA206x-0Wxx Modules				ACB-QCA206x-5Wxx Modules			
FUNCTION	PAD NAME	I/O	PAD #	I/O	PAD NAME	FUNCTION	
Reserved for LAA COEX TXD	Reserved	GPIO_11	76	GPIO_11	LTE_COEX_TXD	LTE Coex Transmit	
3.3V main power supply	3.3V_IN	I	77	I	3.3V_IN	3.3V main power supply	
Ground	GND		78		GND	Ground	
Ground	GND		79		GND	Ground	
Ground	GND		80		GND	Ground	
Ground	GND		81		GND	Ground	
Ground	GND		82		GND	Ground	
3.3V main power supply	3.3V_IN	I	83	I	3.3V_IN	3.3V main power supply	
Ground	GND		84		GND	Ground	
Ground	GND		85		GND	Ground	
Ground	GND		86		GND	Ground	
Ground	GND		87		GND	Ground	
Ground	GND		88		GND	Ground	
Ground	GND		89		GND	Ground	
Ground	GND		90		GND	Ground	
Ground	GND		91		GND	Ground	
Ground	GND		92		GND	Ground	
Ground	GND		93		GND	Ground	
Ground	GND		94		GND	Ground	
Ground	GND		95		GND	Ground	
Ground	GND		96		GND	Ground	
Ground	GND		97		GND	Ground	
Ground	GND		98		GND	Ground	
Ground	GND		99		GND	Ground	
Ground	GND		100		GND	Ground	
Ground	GND		101		GND	Ground	
Ground	GND		102		GND	Ground	
Ground	GND		103		GND	Ground	
Ground	GND		104		GND	Ground	
Ground	GND		105		GND	Ground	
Ground	GND		106		GND	Ground	
Ground	GND		107		GND	Ground	
Ground	GND		108		GND	Ground	
Ground	GND		109		GND	Ground	
Ground	GND		110		GND	Ground	
Ground	GND		111		GND	Ground	
Ground	GND		112		GND	Ground	

PART NUMBER DESCRIPTION

ACB-QCA206X-XXXX ACB-QCA206X-XXXX E20B CoB Pinout map



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