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A

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C

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E

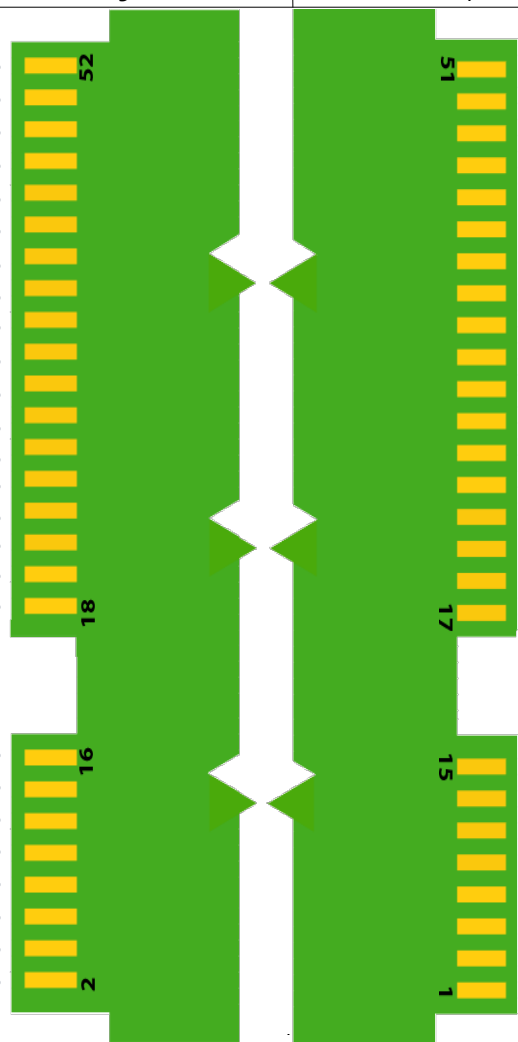
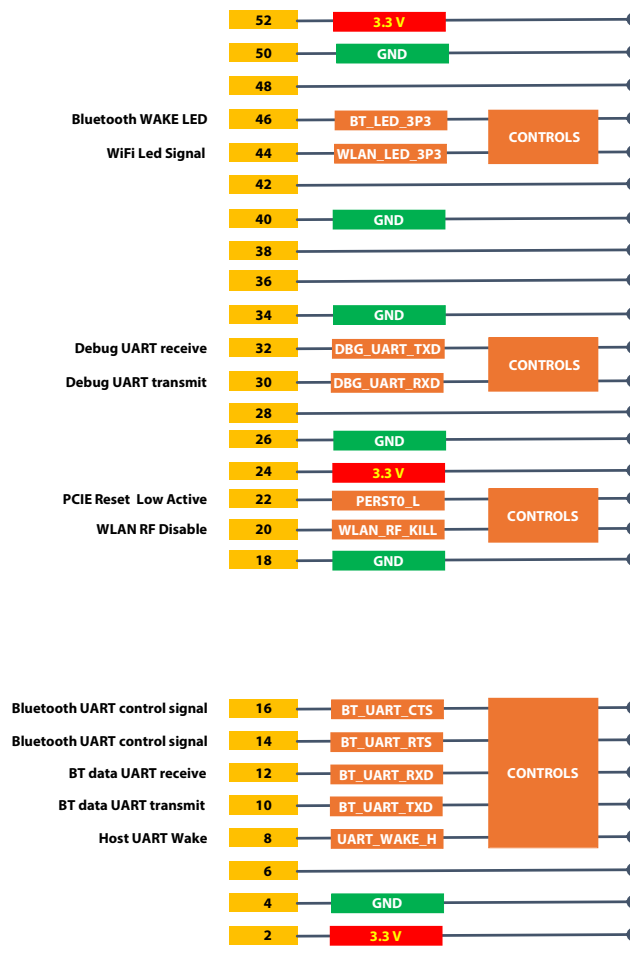
A

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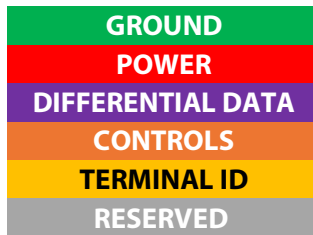
C

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E



**COLOUR CODING**



**PART NUMBER**

AEX-QCA6391-xxx



**DESCRIPTION**

Mini PCIe edge connector pin mapping - Drawing



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DRAWING NUMBER	FINISH by	APVD by	MATERIAL by	UNITS in	DIMENSION	SIZE	SCALE	SHEET
AEBPO-100297- 02	Andy	LT		mm	1PCL ±0.5 ANGLES ±5.	A4	1:1	1 OF 1

RELEASE	Zone	NOTES	DRAWN	CHECKED	DATE
1	1	PCIe for WiFi / UART for BT	AC	LT	2020-01-22
2	1	Pin 23/25 trasposicion correction	AC	LT	2020-07-31

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PIN descriptions	PIN Definition Codes	PIN#	PIN#	PIN Definition Codes	PIN descriptions
3.3V	3.3V aux	52	51	BT_PCM_IN	BT PCM In
Ground	GND	50	49	BT_PCM_OUT	BT PCM Out
open circuit	NC	48	47	BT_PCM_SYNC	BT PCM SYNC
BT_LED_3P3	LED2	46	45	BT_PCM_CLK	BT PCM Clock
WiFi LED	WL_LED_3P3	44	43	GND	Ground
open circuit	NC	42	41	3.3V aux	3.3V
Ground	GND	40	39	3.3V aux	3.3V
open circuit	NC	38	37	GND	Ground
open circuit	NC	36	35	GND	Ground
Ground	GND	34	33	HST_PCIE_RXN	PCIE Receiving Negative
Debug UART transmit	DBG_UART_TXD	32	31	HST_PCIE_RXP	PCIE Receiving Positive
Debug UART receive	DBG_UART_RXD	30	29	GND	Ground
open circuit	NC	28	27	GND	Ground
Ground	GND	26	25	HST_PCIE_TXN	PCIE Transmitting Negative
3.3V	3.3V aux	24	23	HST_PCIE_TXP	PCIE Transmitting Positive
PCIE reset Active Low	PE_RST0_L	22	21	GND	Ground
WiFi RF disable	W_DISABLE1_L	20	19	Wake_BT	BT Wake up
Ground	GND	18	17	W_DISABLE2_L	BT RF disable
	KEY			KEY	
BT UART CTS	BT_UART_CTS	16	15	GND	Ground
BT UART RTS	BT_UART_RTS	14	13	HST_PCIE_REFCLKP	PCIE Reference Clock Positive
BT UART Receive	BT_UART_RXD	12	11	HST_PCIE_REFCLKN	PCIE Reference Clock Negative
BT UART transmit	BT_UART_TXD	10	9	GND	Ground
Host UART Wake	UART_Wake_Host	8	7	CLKREQ0_L	PCIE Clock Request Low Active
open circuit	NC	6	5	LTE_COEX_TXD	LTE Coex Transmit
Ground	GND	4	3	LTE_COEX_RXD	LTE Coex Receive
3.3V	3.3V aux	2	1	PE_WAKE0_L	PCIE Wake up signal Low Active

**PART NUMBER**

AEX-QCA6391-xxx

**DESCRIPTION**

Mini PCIe edge connector pin mapping

TECH DESIGN  
**VOXMICRO**

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**DRAWING NUMBER**

AEBPO-100297- 02

**FINISH by**

Andy

**APVD by**

LT

**MATERIAL by****UNITS in**

mm

**DIMENSION**1PCL ±0.5  
ANGLES ±5.**SIZE**

A4

**SCALE**

1:1

**SHEET**

2 OF 2

**RELEASE****Zone****NOTES****DRAWN****CHECKED****DATE**

1

1

PCIe for WiFi / UART for BT

AC

LT

2020-01-22

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