



COLOUR CODING	
GROUND	
POWER	
DIFFERENTIAL DATA	
CONTROLS	
TERMINAL ID	
RESERVED	
Open Circuit when no indications	

PART NUMBER **DESCRIPTION**
 AFW-QCA6391-xxx M.2 NGFF E-Key edge connector pin mapping - Drawing

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DRAWING NUMBER	FINISH by	APVD by	MATERIAL by	UNITS in	DIMENSION	SIZE	SCALE	SHEET
AEBPO-100242- 03	Andy	LT		mm	1PCL ±0.5 ANGLES ±5.	A4	1:1	1 OF 2

RELEASE	Zone	NOTES	DRAWN	CHECKED	DATE
1	1	PCIe for WiFi / UART for BT	AC	LT	2019-10-24
1	1	MPN refresh	AC	LT	2020-01-22

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E

E

A

B

C

D

E

A

B

C

D

E

PIN descriptions		PIN Definition Codes		PIN#	PIN#	PIN Definition Codes		PIN descriptions	
					75	GND			Ground
	3.3V		3.3V	74	73	N/C			Open circuit
	3.3V		3.3V	72	71	N/C			Open circuit
	open circuit		N/C	70	69	GND			Ground
	open circuit		N/C	68	67	N/C			Open circuit
	open circuit		N/C	66	65	N/C			Open circuit
	Wireless Lan transmitting enable		WL_TX_EN	64	63	GND			Ground
	LTE license assisted access transmitting enable		LAA_TX_EN	62	61	N/C			Open circuit
	Debug UART transmit		DBG_UART_RXD	60	59	N/C			Open circuit
	Debug UART receive		DBG_UART_TXD	58	57	GND			Ground
	WiFi RF disable		W_DISABLE1_L	56	55	PEWAKE0_L			PCIE Wake up signal Low Active
	BT RF disable		W_DISABLE2_L	54	53	CLKREQ0_L			PCIE Clock Request Low Active
	PCIE reset Active Low		PERSTO_L	52	51	GND			Ground
	SUSCLK		SUSCLK	50	49	HST_PClE_REFCLKN			PCIE Reference Clock Negative
	LTE Coex Receive		LTE_COEX_RXD	48	47	HST_PClE_REFCLKP			PCIE Reference Clock Positive
	LTE Coex Transmit		LTE_COEX_TXD	46	45	GND			Ground
	BT Wake up		WAKE_BT	44	43	HST_PClE_TXN			PCIE Transmitting Negative
	open circuit		N/C	42	41	HST_PClE_TXP			PCIE Transmitting Positive
	open circuit		N/C	40	39	GND			Ground
	open circuit		N/C	38	37	HST_PClE_RXN			PCIE Receiving Negative
	BT UART CTS		BT_UART_CTS	36	35	HST_PClE_RXP			PCIE Receiving Positive
	BT UART RTS		BT_UART_RTS	34	33	GND			Ground
	BT UART Receive		BT_UART_RXD	32	31				
				30	29				
				28	27				
				26	25				
				24	23	N/C			Open circuit
	BT UART transmit		BT_UART_TXD	22	21	N/C			Open circuit
	Host UART Wake		UART_Wake_HOST	20	19	N/C			Open circuit
	Ground		GND	18	17	N/C			Open circuit
	BT_LED_3P3 / WAKE_BT		LED2	16	15	N/C			Open circuit
	BT PCM In		BT_PCM_IN	14	13	N/C			Open circuit
	BT PCM Out		BT_PCM_OUT	12	11	N/C			Open circuit
	BT PCM SYNC		BT_PCM_SYNC	10	9	N/C			Open circuit
	BT PCM Clock		BT_PCM_CLK	8	7	GND			Ground
	WiFi LED		WL_LED_3P3	6	5	N/C			Open circuit
	3.3V		3.3V	4	3	N/C			Open circuit
	3.3V		3.3V	2	1	GND			Ground

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