

A

B

C

D

E

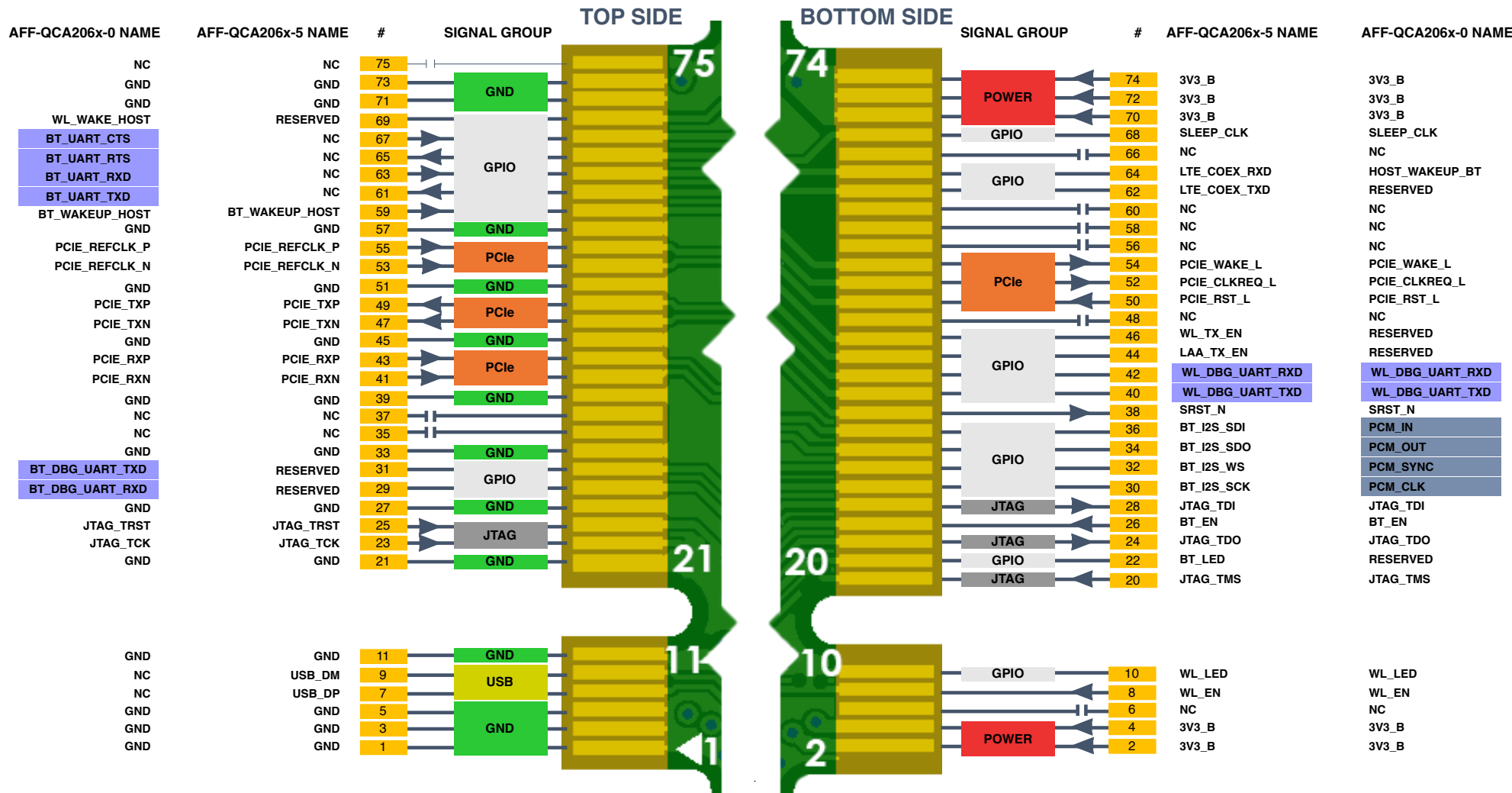
A

B

C

D

E



* Keep floating No Connection [NC] signals and those unused.
** Internal pull-up is recommended for all 3V3 I/Os. All others at VDDIO (1V8).

CONFIDENTIAL AND PROPRIETARY - VOXMICRO, LTD. : NOT TO BE USED, COPIED, REPRODUCED, OR MODIFIED IN WHOLE OR IN PART, NOR ITS CONTENTS REVEALED IN ANY MANNER TO OTHERS WITHOUT THE EXPRESS WRITTEN PERMISSION OF VOXMICRO, LTD.
THIS TECHNICAL DATA MAY BE SUBJECT TO U.S. AND INTERNATIONAL EXPORT, RE-EXPORT OR TRANSFER ("EXPORT") LAWS, DIVERSION CONTRARY TO U.S. AND INTERNATIONAL LAW IS STRICTLY PROHIBITED.

AIRETOS ® AND OxfordTEC ® ARE REGISTERED TRADEMARKS OF VOXMICRO, LTD. ALL OTHER REGISTERED AND UNREGISTERED TRADEMARKS ARE THE PROPERTY OF VOXMICRO LTD GROUP OR THEIR RESPECTIVE OWNERS.

PART NUMBER

AXF-QCA206X-XXXX

**DESCRIPTION**

AXF-QCA206X-XXXX E20 Series F, M.2 B-KEY

Pin map

Copyright © All Rights Reserved - VoxMicro LTD Group



www.voxmicro.com - www.voxmicro.tel

DRAWING NUMBER

AEBPO-190002- 04

FINISH by

Andy

APVD by

LT

MATERIAL by**UNITS in**

mm

DIMENSION

1PCL ±0.5

SIZE

A4

SCALE

1:1

SHEET

1 OF 3

RELEASE**Zone****NOTES****DRAWN****CHECKED****DATE**

2

1

Separation of AFF-QCA206x-0 and AFF-QC206x-5

SC

LT

2023-05-22

3

1

Visual improvements

2023-05-29

4

1

Described reserved pins

SC

LT

2024-01-30

EE

AIRETOS® AND OxfordTEC® ARE REGISTERED TRADEMARKS OF VOXMICRO, LTD. ALL OTHER REGISTERED AND UNREGISTERED TRADEMARKS ARE THE PROPERTY OF VOXMICRO LTD GROUP OR THEIR RESPECTIVE OWNERS.

	1	2	3	4	5	6																									
A	AFF-QCA206x-0Wxx Modules			AFF-QCA206x-5Wxx Modules																											
	FUNCTION	PAD NAME	I/O	PAD #	I/O	PAD NAME	FUNCTION																								
	PCIE Receiving Positive	PCIE_RXP	I	43	I	PCIE_RXP	PCIE Receiving Positive																								
	Reserved for LAA TX EN	Reserved	GPIO_40	44	GPIO_40	LAA_TX_EN	LTE license assisted access transmitting enable. When high the 5GHz receiver is in a protected state.																								
	Ground	GND		45		GND	Ground																								
	Reserved for WL TXEN LAA	Reserved	GPIO_41	46	GPIO_41	WL_TX_EN	Wireless Lan transmitting enable. When high, the LAA receiver is placed in a protected state.																								
	PCIE Transmitting Negative	PCIE_TXN	O	47	O	PCIE_TXN	PCIE Transmitting Negative																								
	No Connection	NC		48		NC	No Connection																								
	PCIE Transmitting Positive	PCIE_TXP	O	49	O	PCIE_TXP	PCIE Transmitting Positive																								
	Wi-Fi PCIE reset signal. Internal pull up 3.3V power domain.	PCIE_RST_L	I	50	I	PCIE_RST_L	Wi-Fi PCIE reset signal. Internal pull up 3.3V power domain.																								
B	Ground	GND		51		GND	Ground																								
	Wi-Fi PCIE Clock Request Low Active. Pull up 3.3V power domain.	PCIE_CLKREQ_L	O	52	O	PCIE_CLKREQ_L	Wi-Fi PCIE Clock Request Low Active. Pull up 3.3V power domain.																								
	PCIE Reference Clock Negative	PCIE_REFCLK_N	I	53	I	PCIE_REFCLK_N	PCIE Reference Clock Negative																								
	Wi-Fi PCIE Wake up signal. Internal pull up 3.3V power domain.	PCIE_WAKE_L	O	54	O	PCIE_WAKE_L	Wi-Fi PCIE Wake up signal. Internal pull up 3.3V power domain.																								
	PCIE Reference Clock Positive	PCIE_REFCLK_P	I	55	I	PCIE_REFCLK_P	PCIE Reference Clock Positive																								
	No Connection	NC		56		NC	No Connection																								
	Ground	GND		57		GND	Ground																								
	No Connection	NC		58		NC	No Connection																								
	Host Bluetooth Wake	BT_WAKEUP_HOST	GPIO_9	59	GPIO_9	BT_WAKEUP_HOST	Host Bluetooth Wake																								
	No Connection	NC		60		NC	No Connection																								
C	Bluetooth UART Transmit Data	BT_UART_TXD	O	61	GPIO_1	NC	No Connection																								
	Reserved for LAA COEX TXD	Reserved	GPIO_11	62	GPIO_11	LTE_COEX_TXD	LTE Coex Transmit																								
	Bluetooth UART Receive Data	BT_UART_RXD	I	63	GPIO_0	NC	No Connection																								
	Host Bluetooth Wake	HOST_WAKEUP_BT	GPIO_10	64	GPIO_10	LTE_COEX_RXD	LTE Coex Receive																								
	Bluetooth UART Request To Send	BT_UART_RTS	O	65	GPIO_3	NC	No Connection																								
	No Connection	NC		66		NC	No Connection																								
	Bluetooth UART Clear To Send	BT_UART_CTS	I	67	GPIO_2	NC	No Connection																								
	External 32.768 KHz sleep clock input	SLEEP_CLK	GPIO_14	68	GPIO_14	SLEEP_CLK	External 32.768 KHz sleep clock input																								
	WiFi Wake Host	WL_WAKE_HOST	GPIO_49	69	GPIO_49	Reserved	Reserved for WL WAKE HOST or WL SEN UART RXD																								
	3.3V main power supply input	3V3_B	I	70	I	3V3_B	3.3V main power supply input																								
D	Ground	GND		71		GND	Ground																								
	3.3V main power supply input	3V3_B	I	72	I	3V3_B	3.3V main power supply input																								
	Ground	GND		73		GND	Ground																								
	3.3V main power supply input	3V3_B	I	74	I	3V3_B	3.3V main power supply input																								
	No Connection	NC		75		NC	No Connection																								
	<table><tr><th>PART NUMBER</th><th>DESCRIPTION</th></tr><tr><td>AXF-QCA206X-XXXX</td><td>AXF-QCA206X-XXXX E20 Series F, M.2 B-KEY</td></tr><tr><td></td><td>Pin map</td></tr><tr><td colspan="2">Copyright © All Rights Reserved - VoxMicro LTD Group</td></tr><tr><td colspan="2"></td></tr><tr><td colspan="2">www.voxmicro.com - www.voxmicro.tel</td></tr></table>							PART NUMBER	DESCRIPTION	AXF-QCA206X-XXXX	AXF-QCA206X-XXXX E20 Series F, M.2 B-KEY		Pin map	Copyright © All Rights Reserved - VoxMicro LTD Group				www.voxmicro.com - www.voxmicro.tel													
	PART NUMBER	DESCRIPTION																													
	AXF-QCA206X-XXXX	AXF-QCA206X-XXXX E20 Series F, M.2 B-KEY																													
		Pin map																													
	Copyright © All Rights Reserved - VoxMicro LTD Group																														
																															
www.voxmicro.com - www.voxmicro.tel																															
<table><tr><th>DRAWING NUMBER</th><th>FINISH by</th><th>APVD by</th><th>MATERIAL by</th><th>UNITS in</th><th>DIMENSION</th><th>SIZE</th><th>SCALE</th><th>SHEET</th></tr><tr><td>AEBPO-190002- 04</td><td>Andy</td><td>LT</td><td></td><td>mm</td><td>1PCL ±0.5</td><td>A4</td><td>1:1</td><td>3 OF 3</td></tr></table>							DRAWING NUMBER	FINISH by	APVD by	MATERIAL by	UNITS in	DIMENSION	SIZE	SCALE	SHEET	AEBPO-190002- 04	Andy	LT		mm	1PCL ±0.5	A4	1:1	3 OF 3							
DRAWING NUMBER	FINISH by	APVD by	MATERIAL by	UNITS in	DIMENSION	SIZE	SCALE	SHEET																							
AEBPO-190002- 04	Andy	LT		mm	1PCL ±0.5	A4	1:1	3 OF 3																							
<table><tr><th>RELEASE</th><th>Zone</th><th>NOTES</th><th>DRAWN</th><th>CHECKED</th><th>DATE</th></tr><tr><td>2</td><td>1</td><td>Separation of AFF-QCA206x-0 and AFF-QC206x-5</td><td>SC</td><td>LT</td><td>2023-05-22</td></tr><tr><td>3</td><td>1</td><td>Visual improvements</td><td></td><td></td><td>2023-05-29</td></tr><tr><td>4</td><td>1</td><td>Described reserved pins</td><td>SC</td><td>LT</td><td>2024-01-30</td></tr></table>							RELEASE	Zone	NOTES	DRAWN	CHECKED	DATE	2	1	Separation of AFF-QCA206x-0 and AFF-QC206x-5	SC	LT	2023-05-22	3	1	Visual improvements			2023-05-29	4	1	Described reserved pins	SC	LT	2024-01-30	
RELEASE	Zone	NOTES	DRAWN	CHECKED	DATE																										
2	1	Separation of AFF-QCA206x-0 and AFF-QC206x-5	SC	LT	2023-05-22																										
3	1	Visual improvements			2023-05-29																										
4	1	Described reserved pins	SC	LT	2024-01-30																										
CONFIDENTIAL AND PROPRIETARY - VOXMICRO, LTD. : NOT TO BE USED, COPIED, REPRODUCED, OR MODIFIED IN WHOLE OR IN PART, NOR ITS CONTENTS REVEALED IN ANY MANNER TO OTHERS WITHOUT THE EXPRESS WRITTEN PERMISSION OF VOXMICRO, LTD. THIS TECHNICAL DATA MAY BE SUBJECT TO U.S. AND INTERNATIONAL EXPORT, RE-EXPORT OR TRANSFER ("EXPORT") LAWS. DIVERSION CONTRARY TO U.S. AND INTERNATIONAL LAW IS STRICTLY PROHIBITED. AIRETOS ® AND OxfordTEC ® ARE REGISTERED TRADEMARKS OF VOXMICRO, LTD. ALL OTHER REGISTERED AND UNREGISTERED TRADEMARKS ARE THE PROPERTY OF VOXMICRO LTD GROUP OR THEIR RESPECTIVE OWNERS.																															
1	2	3	4	5	6																										